

CONTENTS

PREFACE	iii
PART I - ION IMPLANTATION AND DAMAGE ANNEALING	
* PREDICTING LOW ENERGY DOPANT IMPLANT PROFILES IN SEMICONDUCTORS USING MOLECULAR DYNAMICS K. M. Beardmore and N. Gronbeck-Jensen	3
THREE-DIMENSIONAL MONTE-CARLO ION IMPLANTATION SIMULATION FOR MOLECULAR IONS A. Hoessinger, S. Selberherr, and M. Kimura	18
COMPUTATIONALLY EFFICIENT ANALYTICAL MODEL FOR 2-D ION IMPLANTATION D. Li, G. Balamurugan, B. J. Obradovic, G. Wang, Y. Chen, and A. F. Tasch	26
PHYSICALLY BASED MODELS FOR INDIUM AND GERMANIUM ION IMPLANTS INTO SILICON Y. Chen, B. Obradovic, M. Morris, G. Wang, G. Balamurugan, A. F. Tasch, D. Kamenitsa, W. McCoy, S. Baumann, R. Bleier, D. Sieloff, D. Dyer, and P. Zeitzoff	33
* MONTE CARLO SIMULATIONS OF DEFECTS EVOLUTION AND CLUSTERING IN ION-IMPLANTED SI A. La Magna, S. Coffa, S. Libertino, L. Colombo	46
* A NOVEL METHOD TO INVESTIGATE ION-BEAM-INDUCED DEFECT EVOLUTION IN SI M. Posselt	58
DOSE, ENERGY, AND ION SPECIES DEPENDENCE OF THE EFFECTIVE PLUSFACTOR FOR TRANSIENT ENHANCED DIFFUSION G. Hobler, L. Pelaz, and C. S. Rafferty	75
LOCAL DAMAGE ACCUMULATION MODEL FOR ULTRA-LOW ENERGY ION IMPLANTATION J.-W. Kang, K. W. Lee, and H. J. Hwang	87
DIRECT SIMULATION OF ION BEAM INDUCED STRESSING AND AMORPHIZATION OF SILICON K. M. Beardmore and N. Groenbeck-Jensen	96

* Invited Paper

CONTENTS

PART II - DIFFUSION, SEGREGATION AND AGGREGATION

THE DOPANT TRAPPING AND DE-TRAPPING AT THE SiO_2/Si INTERFACE S. Zhao, C.F. Machala, and M.-C. Chang	108
PHOSPHORUS DIFFUSION FROM DOPED POLYSILICON THROUGH ULTRATHIN SiO_2 FILMS INTO Si SUBSTRATES Y. Tsubo, Y. Komatsu, K. Saito, S. Matsumoto, Y. Sato, Y. Yamashita, and I. Yamamoto	116
* THE PIECES OF THE PUZZLE: CHALLENGES IN MODELING OF TRANSIENT ENHANCED DIFFUSION A. H. Gencer and S. T. Dunham	123
A PROCESS SIMULATION MODEL FOR TRANSIENT AND EQUILIBRIUM ARSENIC/PHOSPHORUS CODIFFUSION IN SILICON WITH ZERO COMPUTATIONAL OVERHEAD A. Hoefler	136
ENERGETICS AND KINETICS OF EXTENDED $\{311\}$ DEFECTS IN Si P. Fastenko and S. T. Dunham	143
MODELING OF VACANCY CLUSTER FORMATION IN ION IMPLANTED Si S. Chakravarthi and S. T. Dunham	153
AB INITIO CALCULATIONS OF As-VACANCY INTERACTIONS IN SILICON J. Xie, M. Jones, and S. P. Chen	165

* Invited Paper

CONTENTS

PART III - BACK END PROCESSING

* DISLOCATION DYNAMICS IN SILICON NEAR PATTERNED PADS CORNERS	177
K. Schwarz and D. Chidambarrao	
ATOMISTIC MODELLING OF CU FILM GROWTH	198
Z. Wang, Y. Li, and J. Adams	
LAYOUT-RELATED FAILURES OF DEFECT-FREE ISOLATION SCHEMES FOR ADVANCED CMOS DEVICES	203
I. Peidous, T. Schuelke, R. Sundaresan, and S. Lahiri	
STUDY OF CHEMICAL MECHANICAL POLISHING ON STI (SHALLOW TRENCH ISOLATION) TO OBTAIN LOW DEFECT	215
M. K. Baek, E. G. Chang, C. I. Kim, and S. Y. Kim	
NUMERICAL SIMULATIONS OF SPUTTER DEPOSITION AND ETCHING IN TRENCHES USING THE LEVEL SET TECHNIQUE	220
P. O'Sullivan, F. H. Baumann, and G. H. Gilmer	
AUTHOR INDEX	224
SUBJECT INDEX	226

* Invited Paper